

EXPRESS MAIL LABEL NO. EV314044217US

DATED: 27 JANUARY 2004

ATTORNEY DOCKET: MXIC 1561-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Inventor:

Tzu-Hsuan Hsu et al.

Application No.: _____

Filed: **27 January 2004 (herewith)**

Title: **Circuit and Method for Programming Charge
Storage Memory Cells**

Group Art Unit: **unknown**

Examiner: **unknown**

Customer No. 22470

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is a Form PTO-SB/08. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98. The exception to this, in accordance with the 05 August 2003 Official Gazette Notice in which the USPTO waives the requirement for submitting copies of U.S. Patent and Publications for cases filed after 30 June 2003, is that copies of U.S. Patent documents and copies of U.S. Patent publications are not being submitted.

This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed within three months of the filing date of an application other than a continued prosecution application under 37 C.F.R. 1.53(d).

Fee Authorization. The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (MXIC 1561-1). A copy of this authorization is enclosed.

Respectfully submitted,

HAYNES BEFFEL & WOLFELD LLP

Date: 27 Jan 04

By: 

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Application Number						
Filing Date		27 January 2004 (herewith)				
First Named Inventor		Tzu-Hsuan Hsu et al.				
Group Art Unit						
Examiner Name						
Attorney Docket Number		MXIC 1561-1				
Sheet	1	of	2			

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	A1	5,768,192		Eitan	06-16-1998	
	A2	5,966,603		Eitan	10-12-1999	
	A3	6,011,725		Eitan	01-04-2000	
	A4	6,074,917		Chang et al.	06-13-2000	
	A5	6,219,276		Parker	04-17-2001	
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	A7	6,320,786		Chang et al.	11-20-2001	
	A8	6,396,741		Bloom et al.	05-28-2002	
	A9	6,487,114		Jong et al.	11-26-2002	
	A10					
	A11					
	A12					
	A13					
	A14					
	A15					
	A16					
	A17					
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FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
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Substitute for form 1449B/PTO				C mplete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	
				Filing Date	27 January 2004 (herewith)
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				Examiner Name	
Sheet	2	of	2	Attorney Docket Number	MXIC 1561-1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C1	LUSKY, ELI, et al., "Spatial Characterization of Channel hot electron injection Utilizing subthreshold slope of the localized charge storage NROM memory device," Non-Volatile Semiconductor Memory Workshop, Monterey, CA (Aug 2001), 2 pages	
	C2	LUSKY, ELI, et al., "Electron Discharge Model of Locally-Trapped Charge in Oxide-Nitride-Oxide (ONO) Gates for NROM Non-Volatile Semiconductor Memory Device," SSDM, Tokyo, Japan (Sep 2001), 2 pages.	
	C3	BOAZ, EITAN, et al, "Can NROM, a 2-bit, Trapping Storage NVM Cell, Give a Real Challenge to Floating Gate Cells?" SSDM, Tokyo, Japan (1999).	

Examiner Signature		Date Considered	
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